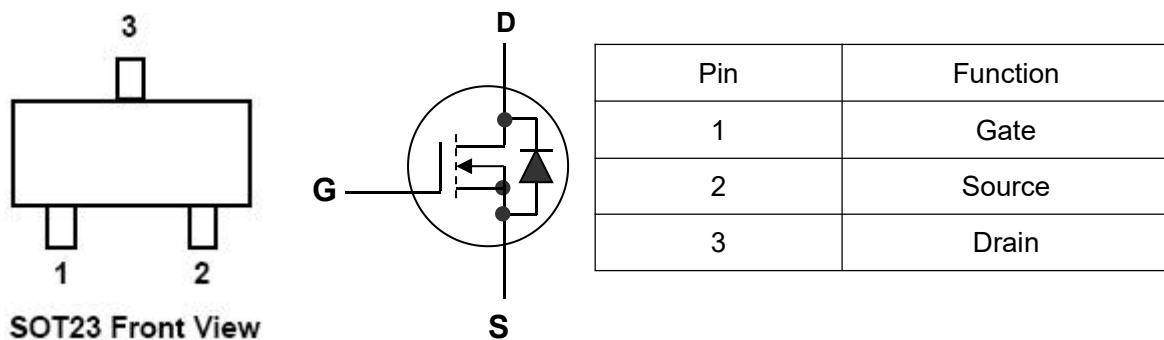


## 1. Features

- $V_{DS}=20V, R_{DS(on)}=30m\Omega(\text{max.}) @ V_{GS}=10V, I_D=6.0A$
- $V_{DS}=20V, R_{DS(on)}=40m\Omega(\text{max.}) @ V_{GS}=4.5V, I_D=3.0A$
- $V_{DS}=20V, R_{DS(on)}=55m\Omega(\text{max.}) @ V_{GS}=2.5V, I_D=2.0A$

## 2. Symbol



## 3. Absolute maximum ratings

( $T_A=25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-source voltage	$V_{DS}$	20	V
Gate-source voltage	$V_{GS}$	$\pm 10$	V
Drain current continuous * $T_J=125^\circ\text{C}$	$I_D$	6.0	A
Drain current pulsed	$I_{DM}$	20	
Power dissipation*	$P_D$	1.25	W
Thermal resistance, junction-ambient	$R_{thJA}$	100	$^\circ\text{C}/\text{W}$
Junction and storage temperature range	$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$

\*Surface mounted on FR 4 board,  $t \leq 10$  sec.

#### 4. Electrical characteristics

( $T_A=25^\circ\text{C}$ ,unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Drain-source breakdown voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	20	-	-	V
Zero gate voltage drain current	$I_{\text{DSS}}$	$V_{\text{DS}}=16\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate- body leakage	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 10\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
Gate threshold voltage*	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	0.5	0.78	1.0	V
On state drain current	$I_{\text{D}(\text{on})}$	$V_{\text{DS}}=5\text{V}, V_{\text{GS}}=4.5\text{V}$	5	-	-	A
Static drain-source on-resistance*	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=6.0\text{A}$	-	25	30	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=3.0\text{A}$	-	35	40	
		$V_{\text{GS}}=2.5\text{V}, I_{\text{D}}=2.0\text{A}$	-	52	55	
Forward transconductance*	$g_{\text{fs}}$	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=5\text{A}$	30	-	-	S
Input capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	888	-	$\text{pF}$
Output capacitance	$C_{\text{oss}}$		-	144	-	
Reverse transfer capacitance	$C_{\text{rss}}$		-	115	-	
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=10\text{V}, I_{\text{D}} = 1.0\text{A}, R_{\text{L}}=10\Omega, R_{\text{G}}=6\Omega, V_{\text{GS}}=4.5\text{V}$	-	31.8	-	$\text{ns}$
Rise time	$t_{\text{r}}$		-	14.5	-	
Turn-off delay time	$t_{\text{d}(\text{off})}$		-	50.3	-	
Fall time	$t_{\text{f}}$		-	31.9	-	
Total gate charge	$Q_{\text{g}}$	$V_{\text{DS}}=10\text{V}, V_{\text{GS}}=4.5\text{V} I_{\text{D}} = 3.5\text{A}$	-	16.8	-	$\text{nC}$
Gate-source charge	$Q_{\text{gs}}$		-	2.5	-	
Gate-drain charge	$Q_{\text{gd}}$		-	5.4	-	
Diode forward voltage	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=1.25\text{A}$	-	0.825	1.3	V
Drain-source diode forward current*	$I_{\text{S}}$		-	-	1.25	A

\*Pulsed test:pulse width $\leq 300\mu\text{s}$ ,duty cycle $\leq 2\%$

## 5. Test circuits and waveforms

